	pplicant(s): Hayashida et al.		- 10 m
	Serial No.: 09/939,870	Group Art Unit: 2836	17.75
	Filed: August 27, 2001 Title: Semiconductor Integrated Circuit Device and Method for Designing the Same	Examiner: Nguyen, Danny	Amolh
		Group Art Unit: 2836 Examiner: Nguyen, Danny	
	Attorney Docket No.: N26180400W		
	RESPONSE TO	OFFICE ACTION	
5	Assistant Commissioner for Patents Washington, D.C. 20231		
	Dear Sir:		
10	The following is submitted in response to currently due <u>January 7, 2003</u> .	o the Office Action dated October 7, 20	02, and is
	Entry of the following amendments is res	spectfully requested.	
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2530	I hereby certify that this correspondence is being	<u>C.R. §1.8</u>	
		ademark Office to fax number:	
	I hereby certify that this correspondence is being I transmitted via facsimile to the United States Patent and Tr	ademark Office to fax number:	
30	I hereby certify that this correspondence is being [] transmitted via facsimile to the United States Patent and Tr Date of Transmittal: [X] deposited with the United States Postal Service as first cla	ademark Office to fax number:	